

In the Claims:

Please cancel claims 1-10 without any disclaimer and a prejudice to and add the following claims.

Listing of claims is as follows:

1-10. (Cancelled)

11. (New) A thin film transistor array substrate for a liquid crystal display, comprising:

an insulating substrate;

a gate line formed on the substrate;

a common electrode line proceeding parallel to the gate line;

a storage capacitor electrode connected to the common electrode line;

a gate insulating layer formed on the gate line, the common electrode line, and the storage capacitor electrode;

a data line formed on the gate insulating layer;

a protective layer formed on the data line; and

a pixel electrode formed on the protective layer with opening patterns,

wherein the pixel electrode covers entire width of the storage capacitor electrodes at particular regions.

12. (New) The thin film transistor array substrate of claim 11, wherein the storage capacitor electrode is respectively provided at left side and right side of each pixel electrode.

13. (New) The thin film transistor array substrate of claim 11, wherein the common electrode line comprises two separate lines.

14. (New) The thin film transistor array substrate of claim 11, wherein each opening pattern comprises a horizontal opening portion formed at the boundary of the pixel electrode bisecting the pixel electrode into an upper region and a lower region, and inclined opening portions formed at the upper region and the lower region of the pixel electrode while proceeding perpendicular to each other.

15. (New) A thin film transistor array substrate, comprising:
an insulating substrate;
a common electrode line with branched electrodes, said common electrode line being formed on the insulating substrate; and
a pixel electrode insulated from the common electrode line and having first opening patterns,
wherein the pixel electrodes cover entire width of the branched electrodes of the common electrode lines at particular regions when viewed from a top side.